

## **ALI TABATABAEI**

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### **Professional Experience**

- 1995-Present    Ph.D Candidate, Center for Integrated Systems, Stanford University. Research area: Bandpass Sigma-Delta modulator; advisor: Prof. Bruce Wooley.
- Designed the frontend circuitry of an A/D converter for CCD application during summer internship in National Semiconductor Corporation. The design included input amplifier, S&H and PGA.
- Designed the Itag section of cache memory during summer internship at SGI.
- 1993 - 95        Member of the High Frequency IC design team through a contractor of Philips Semiconductor, during which:
- Designed a high-speed high slew rate switch for digital data detection
  - Designed a Gm-C filter
  - Designed a fully integrated FM demodulator
  - Designed a traveling wave divider circuit as I & Q signal generator
  - Implemented an interface board for phone line connection to wireless systems

### **Education**

- Present         Fifth year Ph.D. student, Stanford University.
- 1997            M.S. Electrical Engineering, Stanford University.
- 1985 - 93       B.Sc. & M.S. from Sharif University of Technology, Tehran, Iran.

### **Award**

- 1996            Received Kharazmi Award (one of the most prestigious scientific awards in Iran) for contribution in the design and implementation of several IC products.

### **Patent**

- 1995            High-speed/high-slew-rate tri-modal all bipolar buffer/switch and method thereof (US patent # 5,892,376).

## **Publications**

1. A. Tabatabaei, K. Kaviani and B. A. Wooley, "A Two-Path Bandpass Sigma-Delta Modulator with Extended Noise Shaping," ISSCC Digest of Technical Papers, pp. 342-343, Feb. 2000.
2. A. Tabatabaei and B. A. Wooley, "A Wideband Bandpass Sigma-Delta Modulator for Wireless Applications," IEEE 1999 Symposium on VLSI Circuits, June 1999.
3. A. Tabatabaei *et al.*, "A High Slew-Rate Unity-Gain Low-Voltage Buffer with Large Active/Quiescent Current Ratio," IEEE J. Solid-State Circuits, vol. 33, pp.156-163, Jan. 1998.
4. A. Tabatabaei and B. A. Wooley, "A Two-Path Bandpass Sigma-Delta Modulator with Extended Noise Shaping," To be published in JSSC Dec. 2000.

## **Teaching Experience**

- 1997 Teaching assistant for the course: "Digital MOS Integrated Circuits (EE313)", Stanford university.
- 1996 Teaching assistant for the course: "Analog MOS Integrated Circuits (EE315)", Stanford University.
- Teaching assistant for the course: "Digital MOS Integrated Circuits (EE313)", Stanford university.
- 1993 Instructor for Electronics II, Sharif university.

## **Software Experience:**

Programming background in C, C++, PASCAL, FORTRAN.  
Experience in use of HSPICE, MATLAB, SUE, TESS.